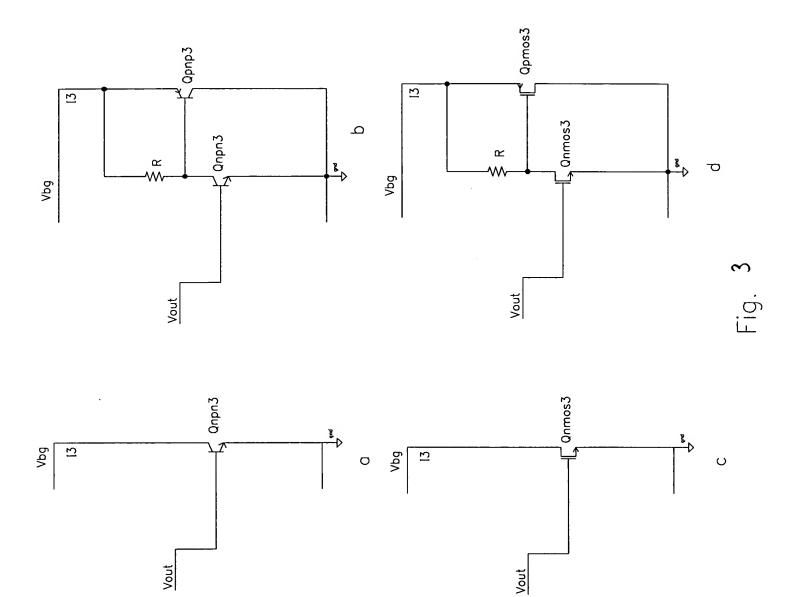
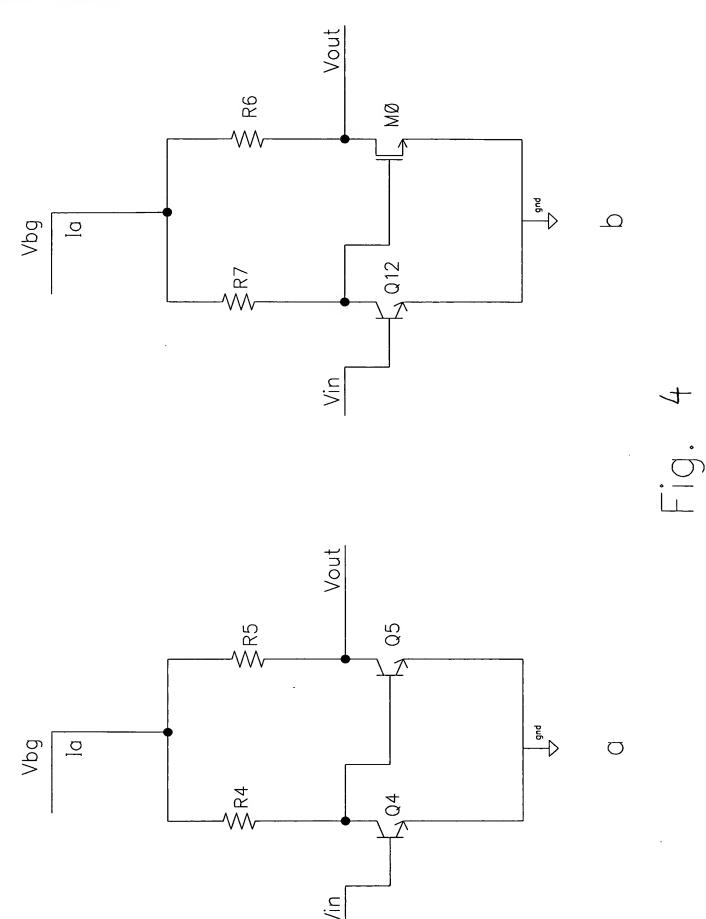
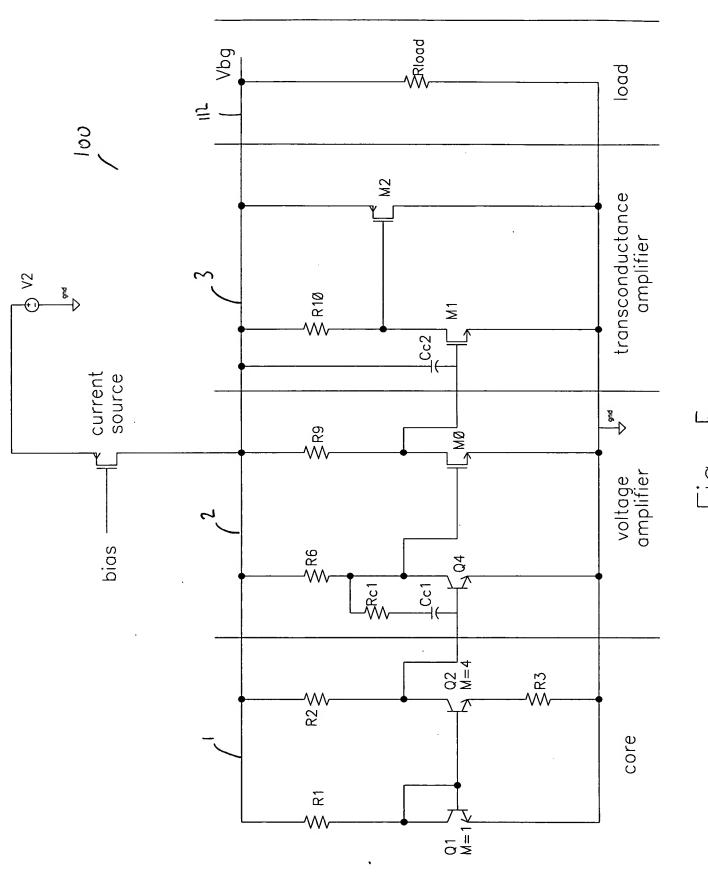


"Band-Gap Reference Circuit With High Power ' Supply Ripple Rejection Ratio" Inventors: Nicolae Marin and Sridhar Kotikalapoodi Atty. Docket: 24317/82751 Sheet 3 of 8



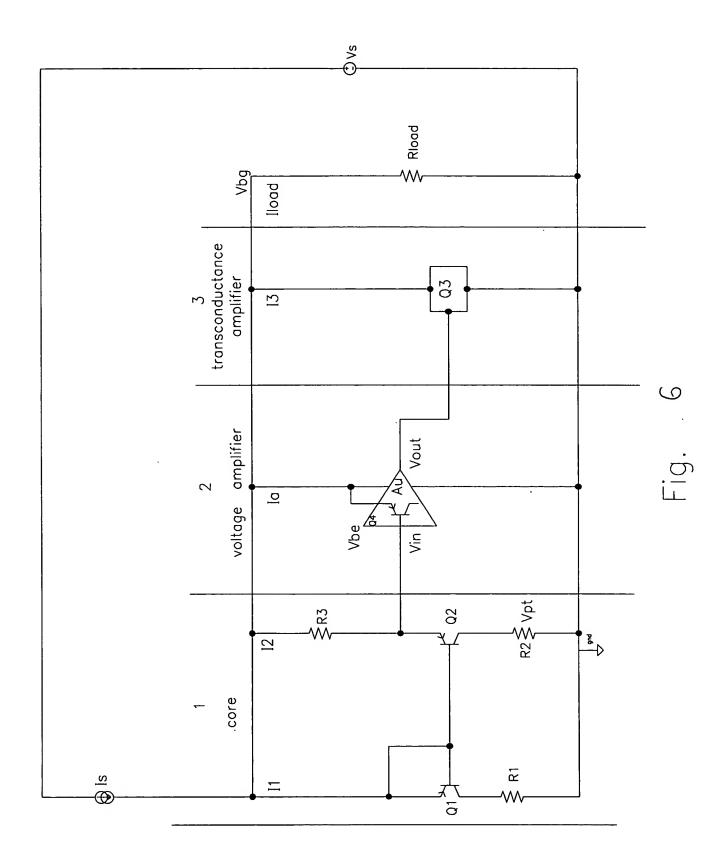
"Band-Gap Reference Circuit With High Power Supply Ripple Rejection Ratio" Inventors: Nicolae Marin and Sridhar Kotikalapoodi Atty. Docket: 24317/82751 Sheet 4 of 8



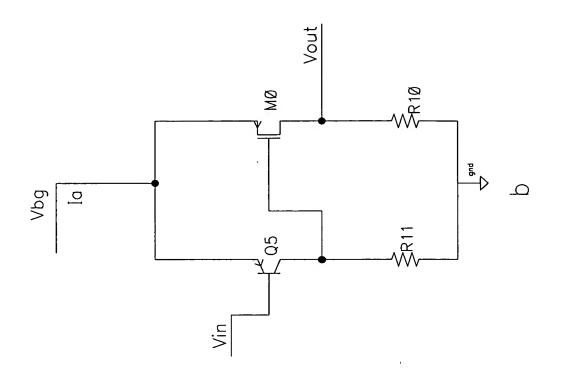


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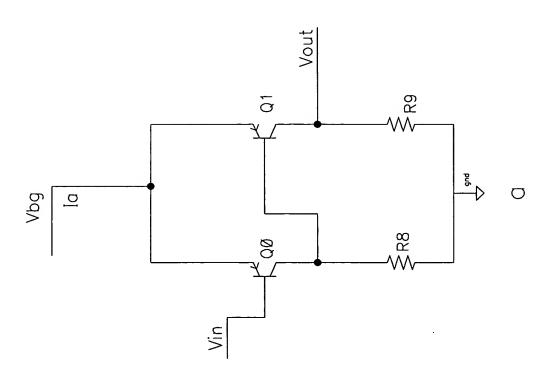
"Band-Gap Reference Circuit With High Power Supply Ripple Rejection Ratio" Inventors: Nicolae Marin and Sridhar Kotikalapoodi Atty. Docket: 24317/82751 Sheet 6 of 8



"Band-Gap Reference Circuit With High Power Supply Ripple Rejection Ratio" Inventors: Nicolae Marin and Sridhar Kotikalapoodi Atty. Docket: 24317/82751 Sheet 7 of 8



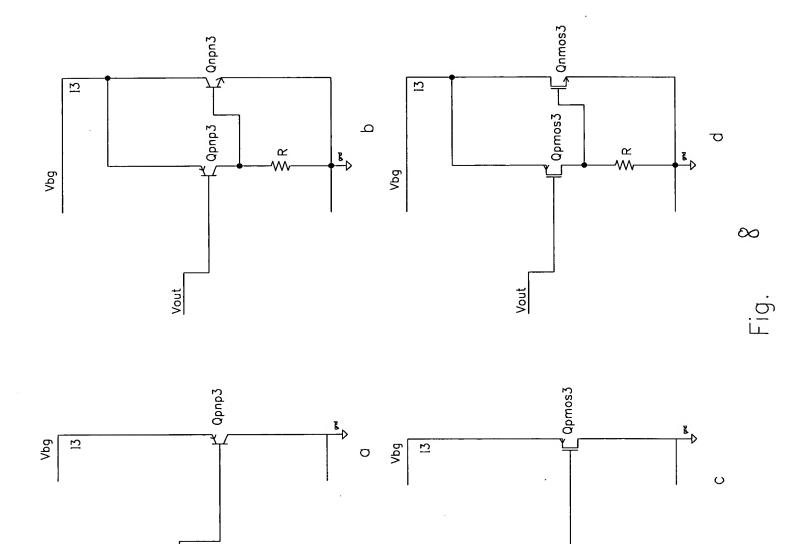




"Band-Gap Reference Circuit With High Power Supply Ripple Rejection Ratio" Inventors: Nicolae Marin and Sridhar Kotikalapoodi Atty. Docket: 24317/82751 Sheet 8 of 8

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